

QT Card Data Format

The mapping of the QT card individual channel FIFOs to the VME bus is shown below.

32-bit longword:

D31 - D24	D23 - D16	D15 - D08	D07 - D00
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QT Data Location:

Q CH 1	T CH 1	Q CH 0	T CH 0
Q CH 3	T CH 3	Q CH 2	T CH 2
Q CH 5	T CH 5	Q CH 4	T CH 4
Q CH 7	T CH 7	Q CH 6	T CH 6

Data are packaged as two channels per long word in the form of QT-pairs for the respective channel. Different sets of FIFOs are read at different addresses. Addresses are contiguous. Data elements, individual Qs or Ts, are 8-bit fields and stored as unsigned integers. The data start at 255 and go to 0, *e.g.*, zero (or the baseline) corresponds to a large integer (255) and a large pulse or large time corresponds to a small integer (0).